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high are each represented by an different frequency of pulse logic 0 is represented by a wave at a specific frequency and logic 1 is represented by a wave at a different frequency

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m trying to connect up it in qucs i sure
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unclear result p s output vt does not
depend on the input bit stream look
please what could be wrong

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555 is capable of generating fsk signal
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digital serial data and we need to
demodulate it to get back the digital
data at the receiver here is the
schematic diagram of the circuit this
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for mark space coding and this
frequency is the answering frequency
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ashutosh bhatt fsk means frequency
shift keying it s one of the modulation
scheme used to transmit digital data
using high frequency carrier signal in
this modulation scheme bit 1 is
transmitted using one carrier signal
and bit 0 is transmitted using another
carrier signal like

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programmable in 63 steps from 20 dbm

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bestselling 1 5 tsi petrol engine and is
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the tsi evo2 in the first models in
europe this engine makes it possible to
reduce both fuel consumption and co2
emissions

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operate on the four stroke cycle and
unless stated otherwise use a wet sump
lubrication system and are water cooled
1

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cars like the volkswagen golf makes use
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